

Low Power System Design

- Why Worry About Power?
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 - Power: The Dominant Design Constraint
 - Chip Architecture and Power Density
 - Battery Storage a Limiting Factor
- Nanometer Transistors and Their Models
 - Emerging devices in the sub-100 nm regime post challenges to low-power design
 - Leakage
 - Variability
 - Reliability
- Power and Energy Basics
- Optimizing Power @ Design Time: Circuit-Level Techniques
- Optimizing Power @ Design Time – Architecture, Algorithms, and Systems
- Optimizing Power @ Design Time – Interconnect and Clocks
- Optimizing Power @ Standby – Circuits and Systems
- Optimizing Power @ Runtime: Circuits and Systems